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- 1 -

Docket: 0756-1551

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: ) RECEIVED  
Shunpei YAMAZAKI et al. ) JUL 27 1998  
Serial No. 08/691,434 ) Art Unit: 1107 GROUP 2500  
Filed: August 2, 1996 )  
For: METHOD OF FABRICATING ) Examiner: Wilczewski  
SEMICONDUCTOR DEVICES )  
AND APPARATUS FOR PROCESSING )  
A SEMICONDUCTOR ) July 1, 1998  
#32  
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GROUP 2500  
JUL 27 1998

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on July 11, 1998.

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## INFORMATION DISCLOSURE STATEMENT

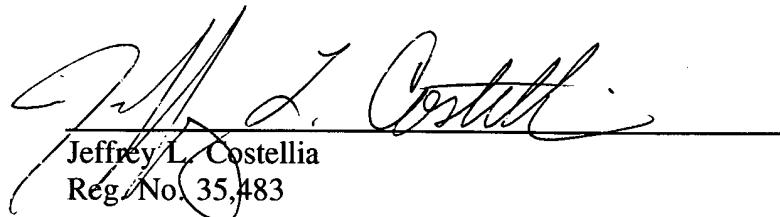
Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. These references were cited in co-pending application Serial No. 08/721,540 and the corresponding parent applications of the '540 application.

A copy of the reference(s) is submitted herewith in accordance with 37 C.F.R. 1.98(a). The Commissioner is authorized to charge Deposit Account No. 19-2380 (0756-1551) for the requisite fee of \$240 in accordance with 37 C.F.R. 1.97(c). A duplicate of this sheet is attached.

Respectfully submitted,



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In re Patent Application of:

JUL 27 1998

Shunpei YAMAZAKI et al.

**GROUP 2500**

Serial No. 08/691,434

) Art Unit: 1107

Filed: August 2, 1996

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For: METHOD OF FABRICATING

) Examiner: Wilczewski

SEMICONDUCTOR DEVICES

)

AND APPARATUS FOR PROCESSING

)

A SEMICONDUCTOR

) July 1, 1998

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GROUP 1300

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Jeffrey L. Costella

**SUBMISSION OF RELATED APPLICATIONS**

Honorable Assistant Commissioner for Patents

Washington, D.C. 20231

**RECEIVED**

Sir:

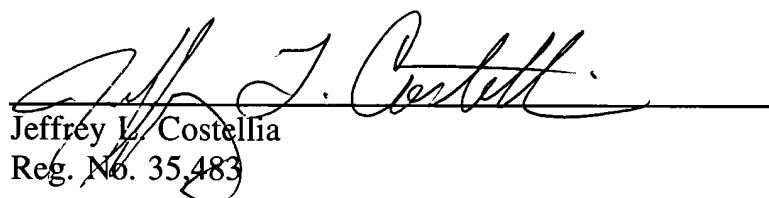
JUL 27 1998  
**GROUP 2500**

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GROUP 1300

Pursuant to Applicant's duty of candor under 37 C.F.R. §1.56, Applicant wishes to inform the Examiner of the following applications directed to related technical subject matter:

<u>Serial No.</u>	<u>Filing Date</u>	<u>Title</u>
08/721,540	09/26/96	Method For Annealing A Semiconductor

Respectfully submitted,



Jeffrey L. Costellia  
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